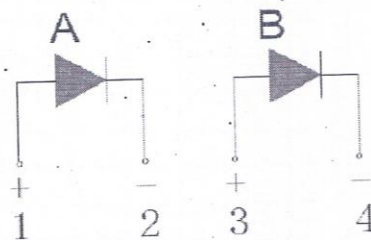


INTERNAL CIRCUIT DIAGRAM



CODE D

** NOTE : PIN SPACING IS 5.5MM

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

PARAMETER	SYMBOL	MAXIMUM RATING	UNIT
Power Dissipation	PD	72	mW
Peak Forward Current (1/10 Duty Cycle, 0.1 Ms Pulse Width)	IPEAK	90	mA
DC Forward Current	IF	30	mA
Reverse Voltage	VR	5	V
Operating Temperature Range	TA	-40°C to +85°C	
Storage Temperature Range	TSTG	-40°C to +85°C	
Solder temperature 1/16 inch below seating plane for 3 seconds at 260°C			

ELECTRICAL OPTICAL CHARACTER AND CURVES (Ta = 25°C)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	LOCATION	TEST CONDITION
Forward Voltage	V _F	-	2.10	2.40	V	Per Chip	I _F = 20mA
Luminous Intensity	I _v	100	115	130	mcd	Per Chip	I _F = 20mA
Peak Emission Wavelength	λ _p	-	635	-	nm	Per Chip	I _F = 20mA
Dominant Emission Wavelength	λ _d	620	625	630	nm	Per Chip	I _F = 20mA
Spectral Line Half-Width	Δλ _{1/2}	-	20	-	nm	Per Chip	I _F = 20mA
Reverse Current	I _R	-	-	10	uA	Per Chip	V _R = 5V

■ **Typical Electro-Optical Characteristic Curve:**

FIG. 1 Forward Current Vs. Forward Voltage

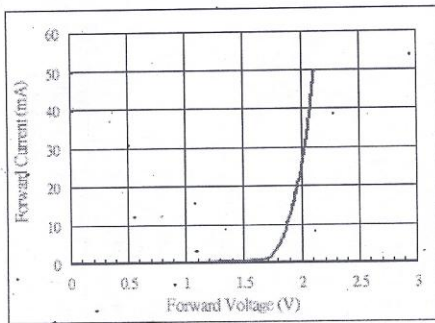


FIG. 2 Relative Intensity Vs. Forward Current

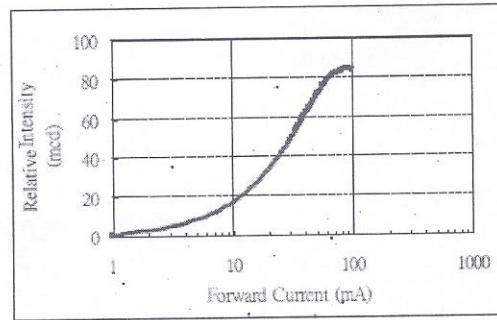


FIG. 3 Forward Voltage Vs. Temperature

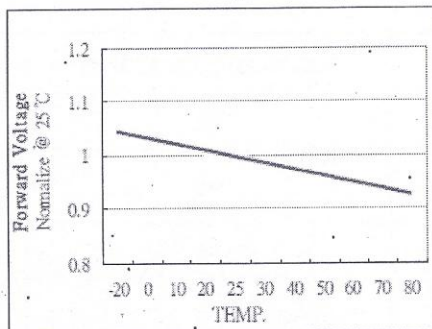


FIG. 4 Relative Intensity Vs. Temperature

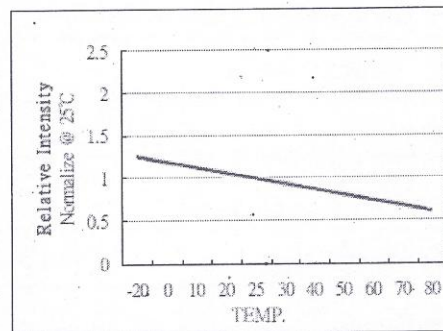
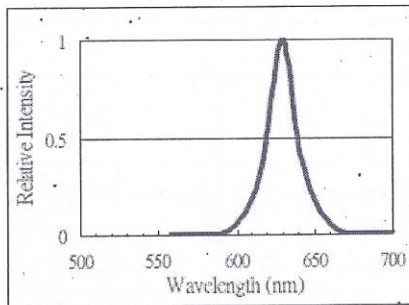


FIG. 5 Relative Intensity Vs. Wavelength



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